

(ELE 5303-5)
B.Sc. (MECS) (CBCS) Examinations
OCTOBER - 2019
EXAMINATION AT THE END OF SEMESTER- V
PART - II ELECTRONICS
MICRO PROCESSORS & (INTEL 8085)

TIME : Two and half hours

Maximum : 60 Marks

Section – A

Answer any five out of 10 questions.

5 X 4 M = 20 M

1. Explain register array in 8085 microprocessor.
2. Explain maskable and non maskable interrupts.
3. Explain data transfer instructions.
4. Explain timing diagrams for OP CODE fetch.
5. Write an ALP for addition of two 8-bit numbers.
6. Write an ALP for subtraction of two 8-bit numbers.
7. Write the differences between memory mapped I/O and I/O mapped I/O.
8. Explain 2KX8 RAM interfacing to 8085 microprocessor.
9. Draw the pin diagram of 8255.
10. Explain about simple temperature control.

Section – B

Answer all questions.

5 X 8 M = 40 M

11. (A) Draw the block diagram of 8085 microprocessor and explain.
Or
(B) Explain about hardware interrupts in 8085.
12. (A) Explain timing diagrams for memory read and write.
Or
(B) Explain addressing modes of 8085 microprocessor with examples.
13. (A) Write an ALP for smallest number in an array.
Or
(B) Explain stack and subroutine.
14. (A) Explain 4KX8 ROM interfacing to 8085 microprocessor.
Or
(B) Explain 4KX8 RAM interfacing to 8085 microprocessor.
15. (A) Explain the operating modes of 8255.
Or
(B) Draw the block diagram of key board and display interface 8279.